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Atty. Docket No.: 2002P17761US (INF-021)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Roger D. Arnold, et al

Application No.: 10/774,038 Group No.:2151

Filing Date: February 6, 2004 Examiner: Unknown

For: "Thread Selection Unit And Method To Fairly Allocate Processor Cycles In A Block

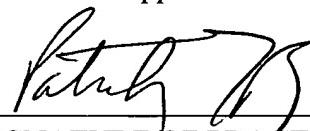
Multithreaded Processor"

Date: April 6, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

STATUS INQUIRY

1. More than twelve months have passed since the filing of this application on February 6, 2004. No communication has been received from the Patent and Trademark Office indicating action on this application.
2. Kindly advise the undersigned of the present status of this application.



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SIGNATURE OF PRACTITIONER

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